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## APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

*None THIS*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

*Verified RMB*

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IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>[Signature]</i> Examiner's Signature	<i>[Initials]</i> Initials			

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## TITLE

Method of manufacturing semiconductor device having damascene interconnection

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees
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